** Sinhgad Technical Education Society’s**

**Sinhgad INSTITUTE OF TECHNOLOGY**

(Affiliated to University of Pune and Approved by, AICTE, New Delhi.)

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**Department of Electronics and Telecommunication Engineering**

**Oral Question Bank**

**Unit II : Compiler, Loaders and Linkers**

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| Sr. No. | **Unit:II** **Question** |
|  | Define Compiler |
|  | What are the basic of function compiler ? |
|  | Give the list of compiler phases? |
|  | What is memory allocation. |
|  | Explain Code Optimizations |
|  | Explain preemptive and non preemptive concept with example**.** |
|  | What is Loader? |
|  | What is function of Loader? |
|  | Which are the different loader Schemes? |
|  | What is Allocation? |
|  | What is relocation? |
|  | What are the advantages and Limitations of Absolutes |
|  | Define Linker. |
|  | How to calculate relocation factor. |
|  | Explain Static and dynamic linker. |

**Prof.D.N.DUCHE**